

## DBL-003-1015027

Seat No. \_\_\_\_\_

## B. Sc. (Physics) (Sem. V) Examination

June - 2022

P-503 : Solid State Electronics (Old Course)

Faculty Code: 003 Subject Code: 1015027

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Time	e : 2	$2\frac{1}{2}$ Hours] [Total Marks : 7	70			
Inst	ruct	<ul> <li>ions: (1) Symbols and notations have their usual meanin</li> <li>(2) Total marks of the question is indicated on the right side of the question.</li> <li>(3) Attempt any five questions out of the following ten.</li> </ul>	ie			
1	(a)	Answer the following questions in short:  (1) 1 bel = dB.  (2) Coupling capacitor is also known as  (3) When the band width (BW) is multiplied by the gain at mid frequencies, gain band width (GBW) is obtained. (True / False)  (4) TC coupled amplifier is use for amplify low frequency signals. (True / False)	4			
	(b)	Answer the following question: A two stage amplifier has a first stage voltage gain of 100, and second stage voltage gain of 200. Find the total voltage gain in dB.	2			
	(c) (d)	Explain frequency response of R-C coupled amplifier. Write a short note on the role of capacitors in transistors amplifier.	3 5			
2	(a)	<ul> <li>Give answer in very short:</li> <li>(1) Power amplifier is meant to decrease the power level of the input signal. (True / False)</li> <li>(2) In the voltage amplifier transistor with high β is used in the circuit. (True / False)</li> <li>(3) The change of output wave shape from the input wave shape of an amplifier is known as</li> <li>(4) The unit of thermal resistance (θ) is</li> </ul>	4			

DBL	<b>-003</b> -	1015027 ] 2 [ Conto	ł
	(c) (d)	Explain how a transistor works as switch. Explain in detail biased clippers.	3 5
		and 3 $K_{\Omega}$ load resistance, is applied with an input having peak voltage +10V. Find the peak output voltage.	
	(b)	(4) In transistor shunt voltage regulator output voltage is equal to the A negative shunt clipper made with 1 $K_{\Omega}$ resistance	2
		(3) In transistor series voltage regulator output voltage is given by $V_{out} = V_z - V_{BE}$ . (True / False)	
		$R_{L(min)} = V_{FL} \times I_{FL}$ (true / false)  (2) The Zener diode in voltage regulation must operate in the breakdown region. (True / False)	
4	(a)	Give answer in short: (1) The minimum Load resistance.	4
	(d)	Draw the neat circuit diagram of an astable multivibrator and explain its working.	5
	(c)	Explain a differential circuit.  Draw the post givenit diagram of an estable	3
	(b)	An astable multivibrator having $R_1=R_2=12~{\rm K}\Omega$ and $C_1=C_2=0.02~\mu F$ . Find the time period and	2
		without external triggering is known as multivibrator.  (4) In monostable multivibrator the width of duration of pulse at the collector of either transistor is given by T=R <sub>2</sub> C <sub>1</sub> .	
		<ol> <li>A circuit that can turn ON and OFF power to an electrical circuit is called</li> <li>The transistor in saturation and cutoff states behaves as a switch. (True / False)</li> <li>A multivibrator that continually changes its states</li> </ol>	
3	(a)	in brief.  Give the answers in very short:	4
	(c) (d)	<ul> <li>(i) ac output voltage</li> <li>(ii) ac contput current.</li> <li>Explain thermal runaway.</li> <li>Give the classification of power amplifier and explain</li> </ul>	3 5
	(b)	A power amplifier supplies 50W to an $8\Omega$ speaker find :	2

5	(a)	Give answer in short:	4
		(1) An integrated circuit (1C) is also known as	
		(2) The first IC was developed by	
		(3) The IC is not repairable. (True / False)	
		(4) In large scale of integration (LSI) component density is	
	(b)	Explain the basic idea of clamper.	2
	(c)	Explain the type of voltage regulators.	3
	(d)	Explain the Zener diode as a voltage regulator.	5
6	(a)	Give answer in short:	4
		(1) Draw symbol of operational amplifier.	
		(2) What is the full form of CMRR?	
		(3) For ideal op-amp the open-loop gain Av is	
		(4) The adder circuit gives an output voltage equal	
		to the algebraic sum of two or more input voltage. (True / False)	
	(b)	If the dc output voltage is 400 V with no-load attached	2
	` ´	to power supply but decreases to 300 V at full load,	
		find the percentage voltage regulation.	
	(c)	Explain ordinary dc power supply.	3
	(d)	Write note on series voltage regulator.	5
7	(a)	Give answer in short:	4
		(1) is device that converts energy in one form to the another form.	
		(2) Technometer is a device which converts into	
		an electrical signal.	
		(3) Give full form of LVDT.	
		(4) Strain gauge is transducer.	
	(b)	Explain common mode and differential mode signals.	2
	(c)	Explain : Op-Amp as unity follower.	3
	(d)	Derive an expression for the output voltage of an	5
		Op-amp differentiator.	
8	(a)	Give answer in short:	4
		(1) Thernistar is the short form for	
		(2) What is the full form of RTD?	
		(3) Gauge Factor (K) defined as the fractional ratio	
		of the change in resistance to the change in length.  True / False.	
		(4) Quartz crystal is used for high frequencies in crystal microphone. True/False	
DR	L-003	-1015027 ] 3 [ Conto	d
	_ 555	[ Control	

	(b)	If $R_1 = 10 \text{ K}_{\Omega}$ , $R_f = 100 \text{ K}_{\Omega}$ , $V_1 = 5V$ and $V_2 = 6V$ , find the output voltage for the op-amp subtractor	2
		circuit.	
	(c)	Explain resistance temperature detector.	3
	(d)	Explain construction and working of LVDT.	5
9	(a)	Give answer in short:	4
		(1) The deflection type of instruments are analog instruments. (True / False)	
		(2) The multimeter is also known as	
		(3) What is the full form of VTVM?	
		(4) Give the full form of CRO.	
	(b)	Describe the three ways of making measurements.	2
	(c)	Explain analog and digital instruments.	3
	(d)	Explain DVM with proper block diagram.	5
10	(a)	Write answer in short:	4
		(1) If Half-Adder the output of the Ex-OR gate is called	
		(2) Multiplexer is also called	
		(3) In NIBBLE MULTIPLEXER when SELECT is	
		high, the right nibble is select to the output.	
		(True / False)	
		(4) BCD is an abbreviation for	
	(b)	Determine the frequency of oscillation for the astable	2
		555 timer. Circuit specifications are RA = RB = 47 K $\Omega$	
		and $C = 1000 \text{ PF}.$	
	(c)	Explain R/S flip-flop with the circuit diagram and	3
		truth-table.	
	(d)	Write a note on multiplexer and demultiplexer.	5